

ABSTRACT OF THE DISCLOSURE

A PDP driving circuit for a stable operation of a ramp pulse. A capacitor having a temperature characteristic opposite to a temperature characteristic of a part coupled to a switch that operates as a constant current source for generation of a ramp pulse is arranged in the driving circuit for generating a ramp pulse. The ramp pulse linearly increases or decreases a panel voltage of the PDP with respect to time. Parts having opposite temperature characteristics are coupled in parallel to control variation of a gradient of the ramp pulse so that values of the parts may not be varied depending on the temperature changes. Thus, stable operation of the ramp pulse is obtained.

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